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RULE				

APPLICANTS

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** CONTINUING DATA *****

CR NONE

** FOREIGN APPLICATIONS *****

JPN
JAPAN 2001-269216 09/05/2001

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Foreign Priority claimed	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no	STATE OR COUNTRY JAPAN	SHEETS DRAWING 14	TOTAL CLAIMS 44	INDEPENDENT CLAIMS 4
35 USC 119 (a-d) conditions met	<input type="checkbox"/> yes <input type="checkbox"/> no	Met after Allowance			
Verified and Acknowledged	<i>Edward Fox and Cahn</i>	Initials			
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ADDRESS

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TITLE

Test circuit and semiconductor integrated circuit effectively carrying out verification of connection of nodes

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